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NOV 04 2003  
PATENT & TRADEMARK OFFICE

"Memory Device Having A Programmable Register"  
Applicant: Farmwald, et al.  
Application No. 09/801,151  
Attorney Docket No. 57841.000026

6697295

FIG\_1

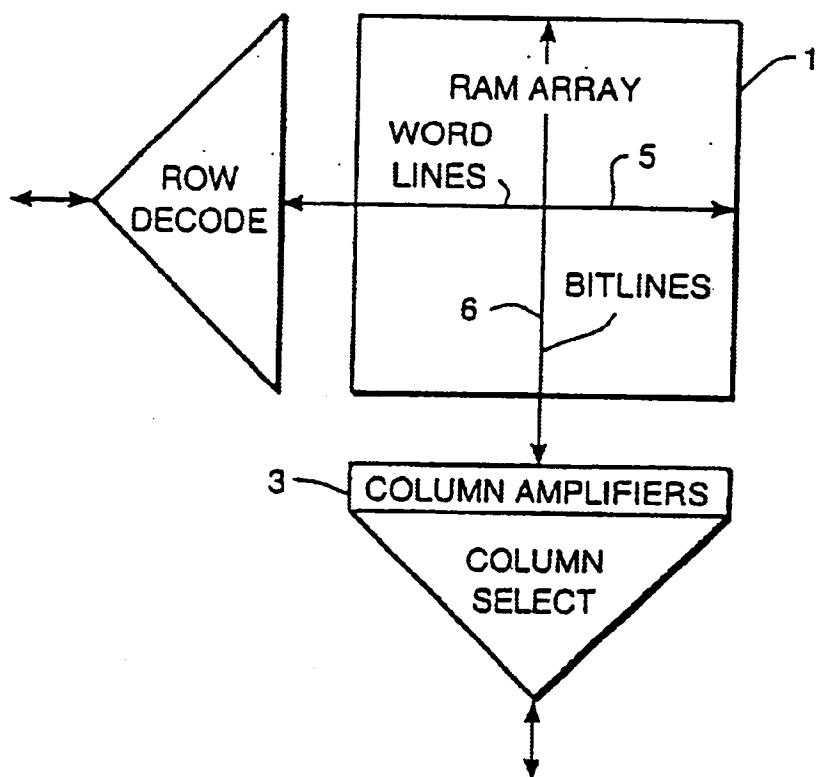
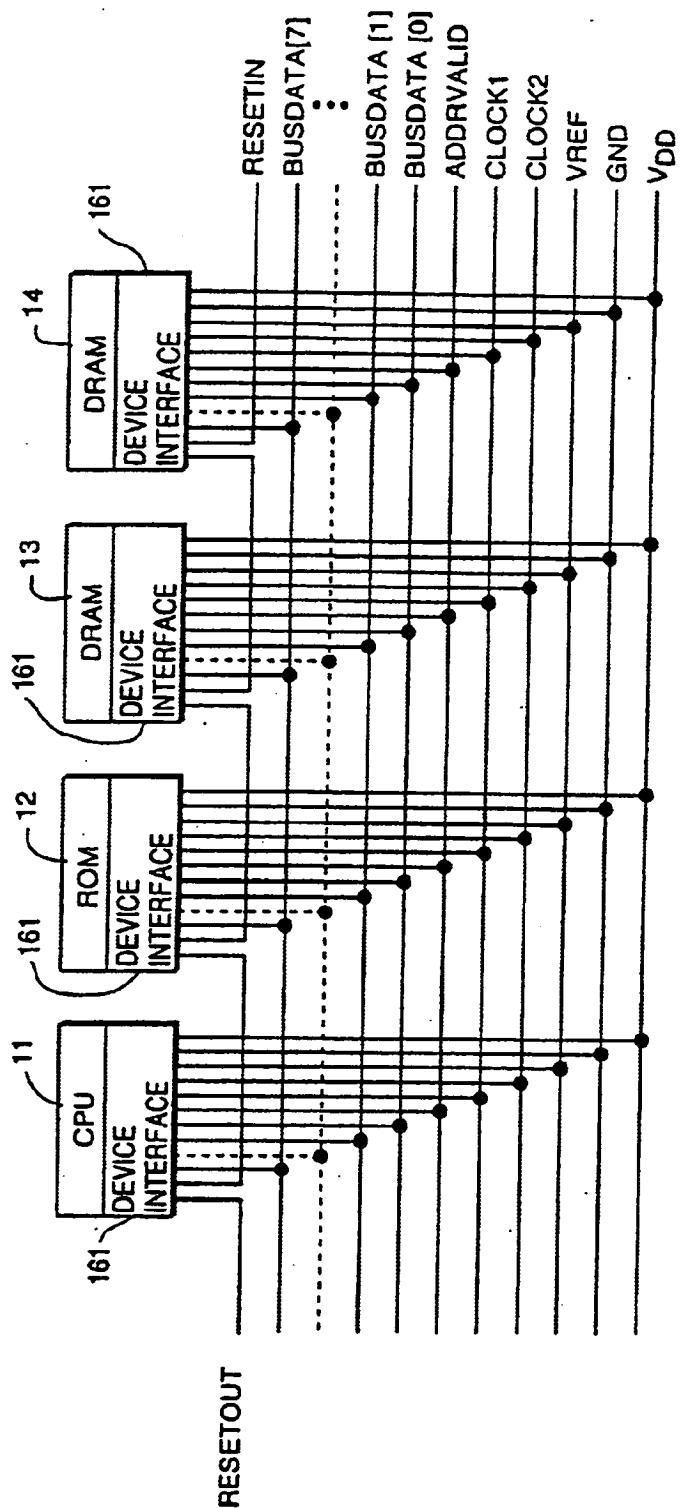
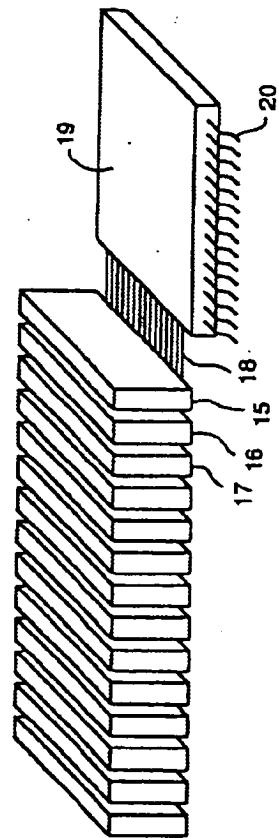


FIG - 2



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FIG. 3



REGULAR ACCESS

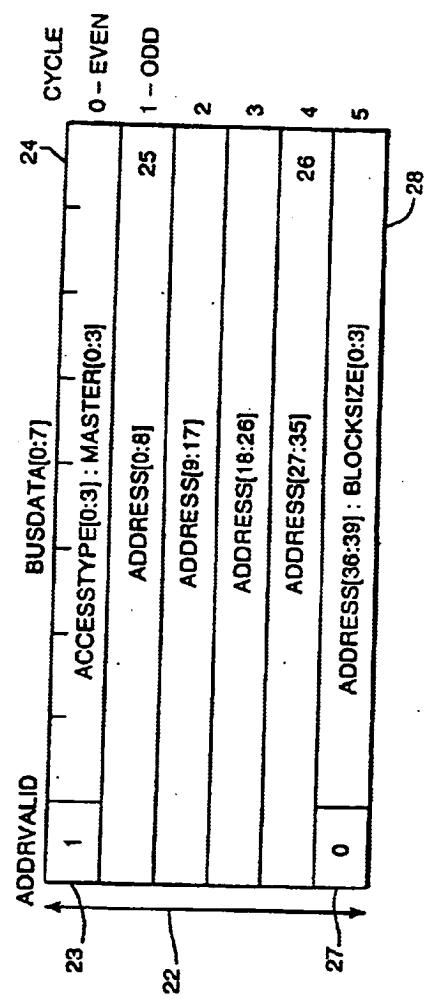
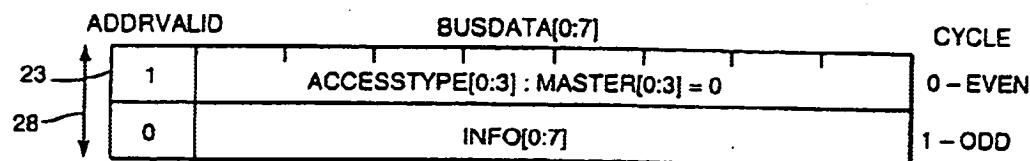


FIG. 4

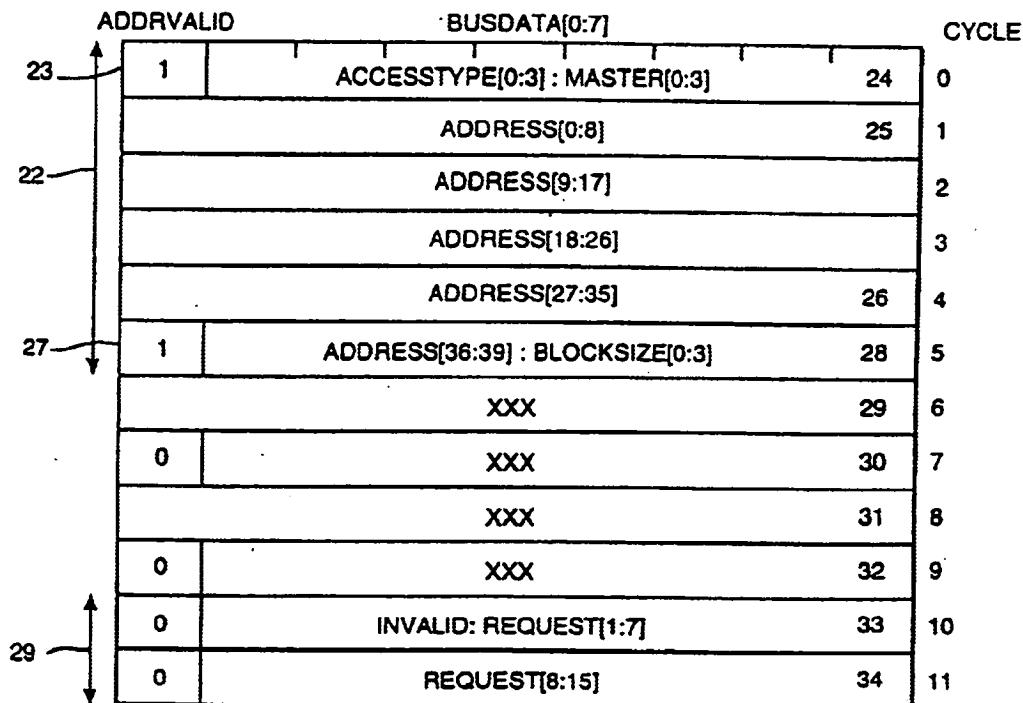


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### REJECT (NACK) CONTROL PACKET



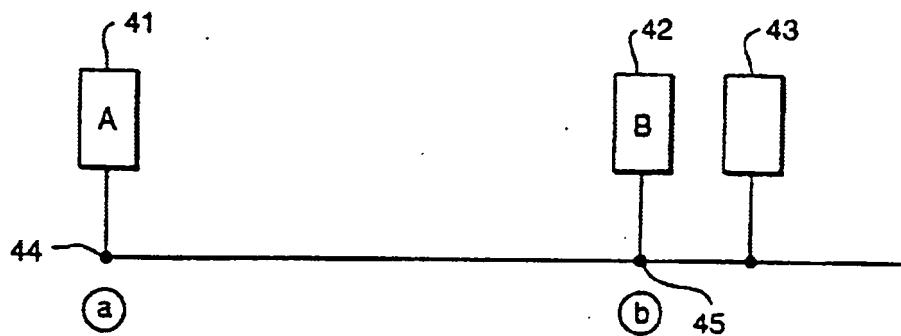
**FIG\_5**



**FIG\_6**

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FIG\_7A

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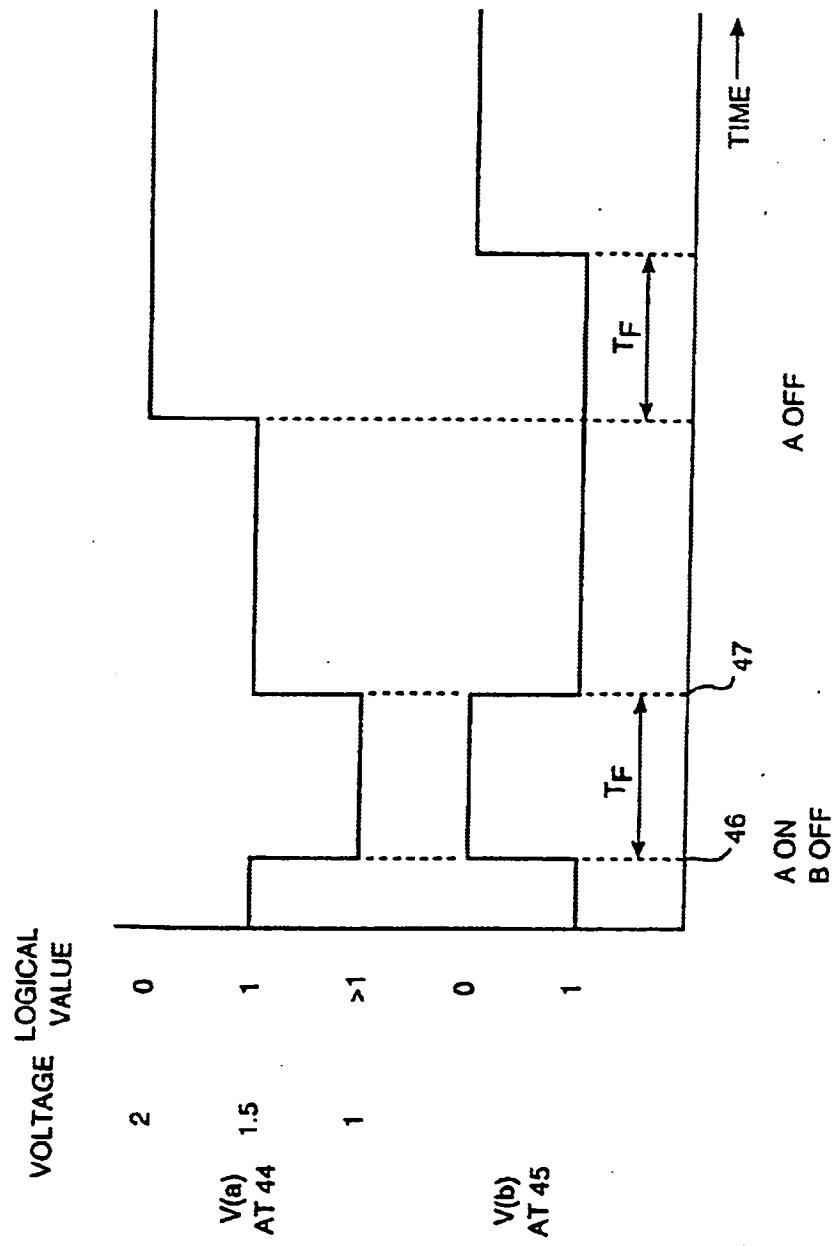
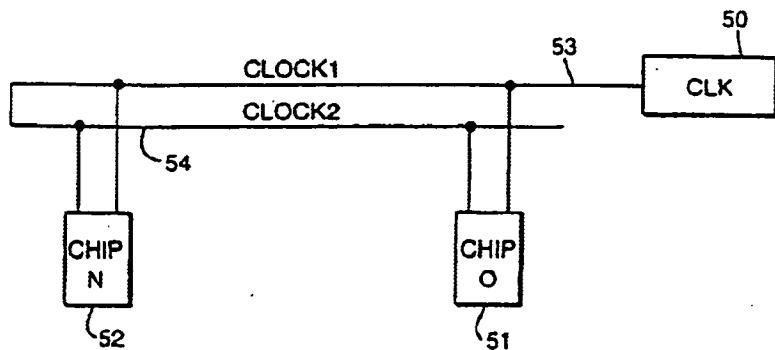


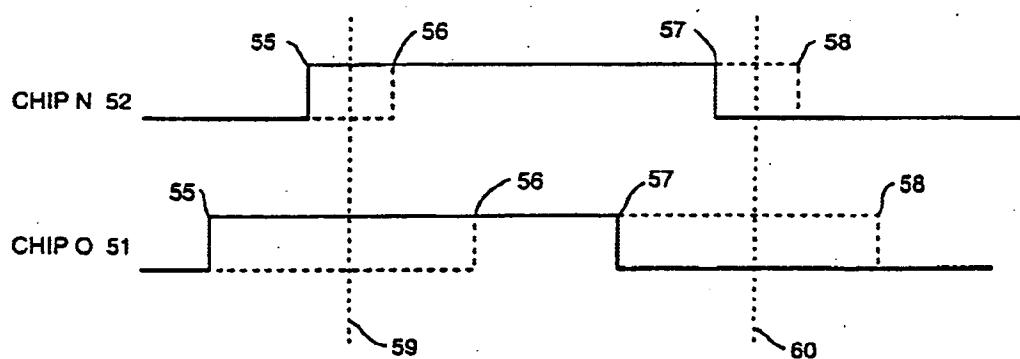
FIGURE 7B

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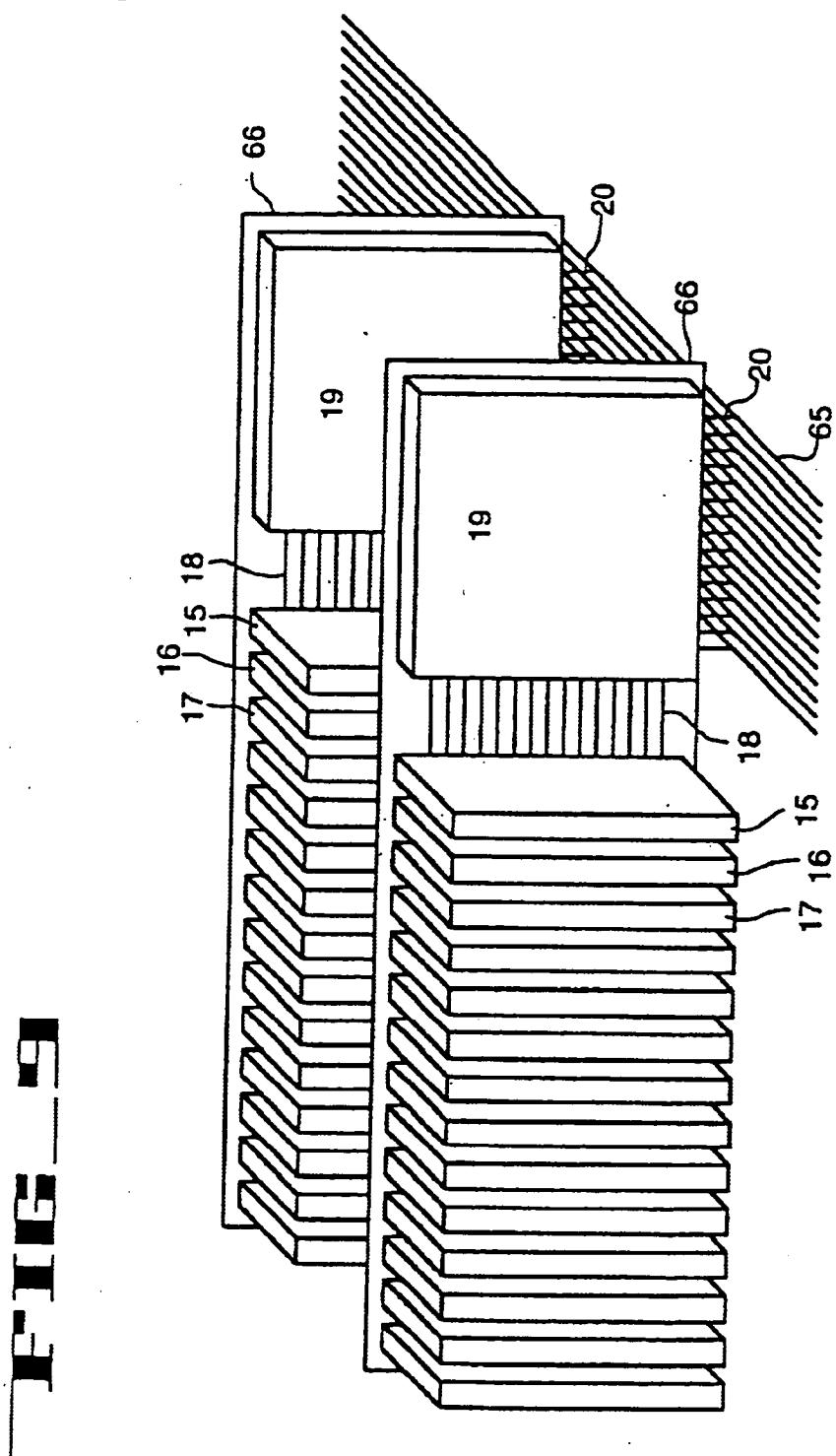
FIG\_8A



FIG\_8B

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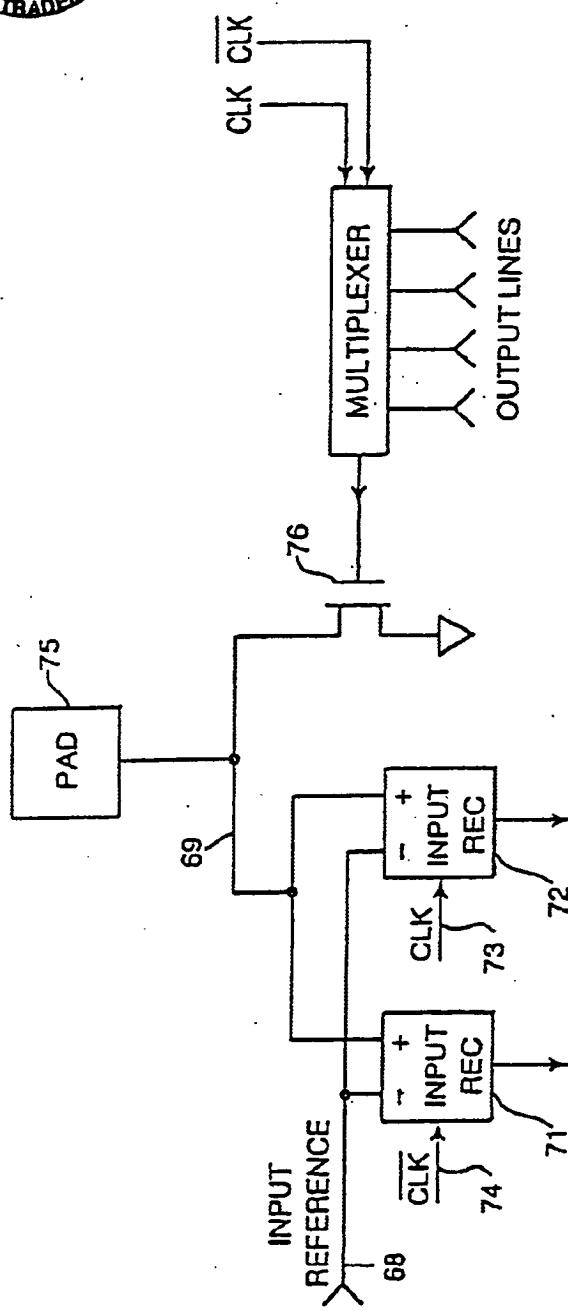
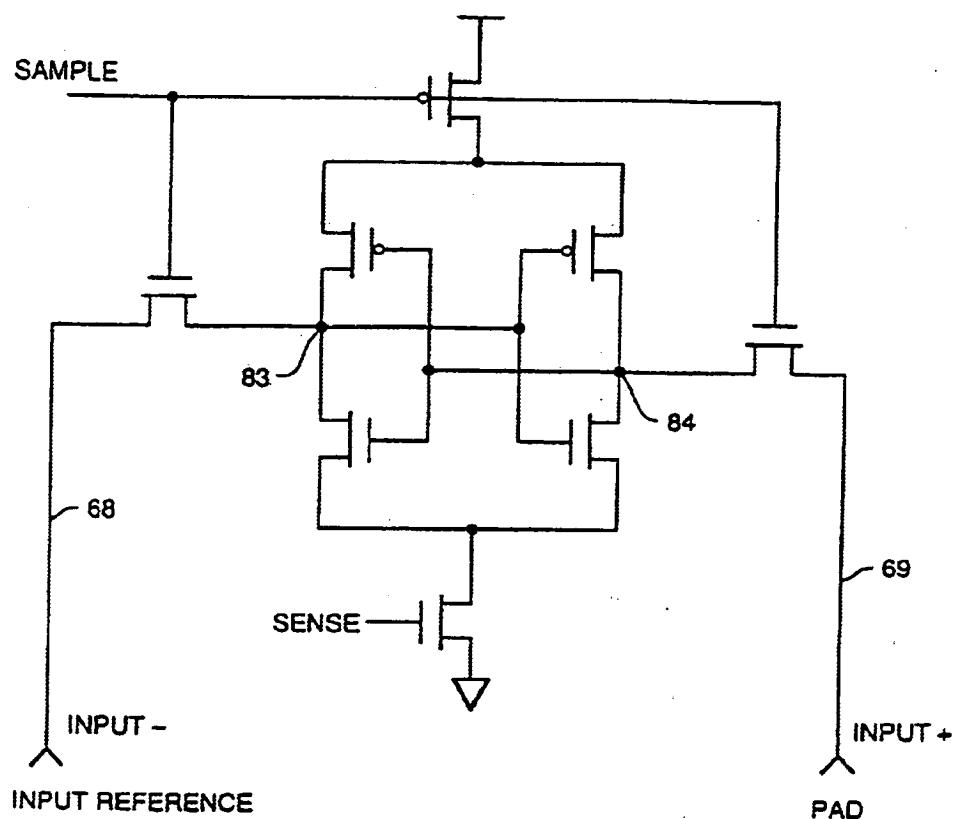


FIG - 10

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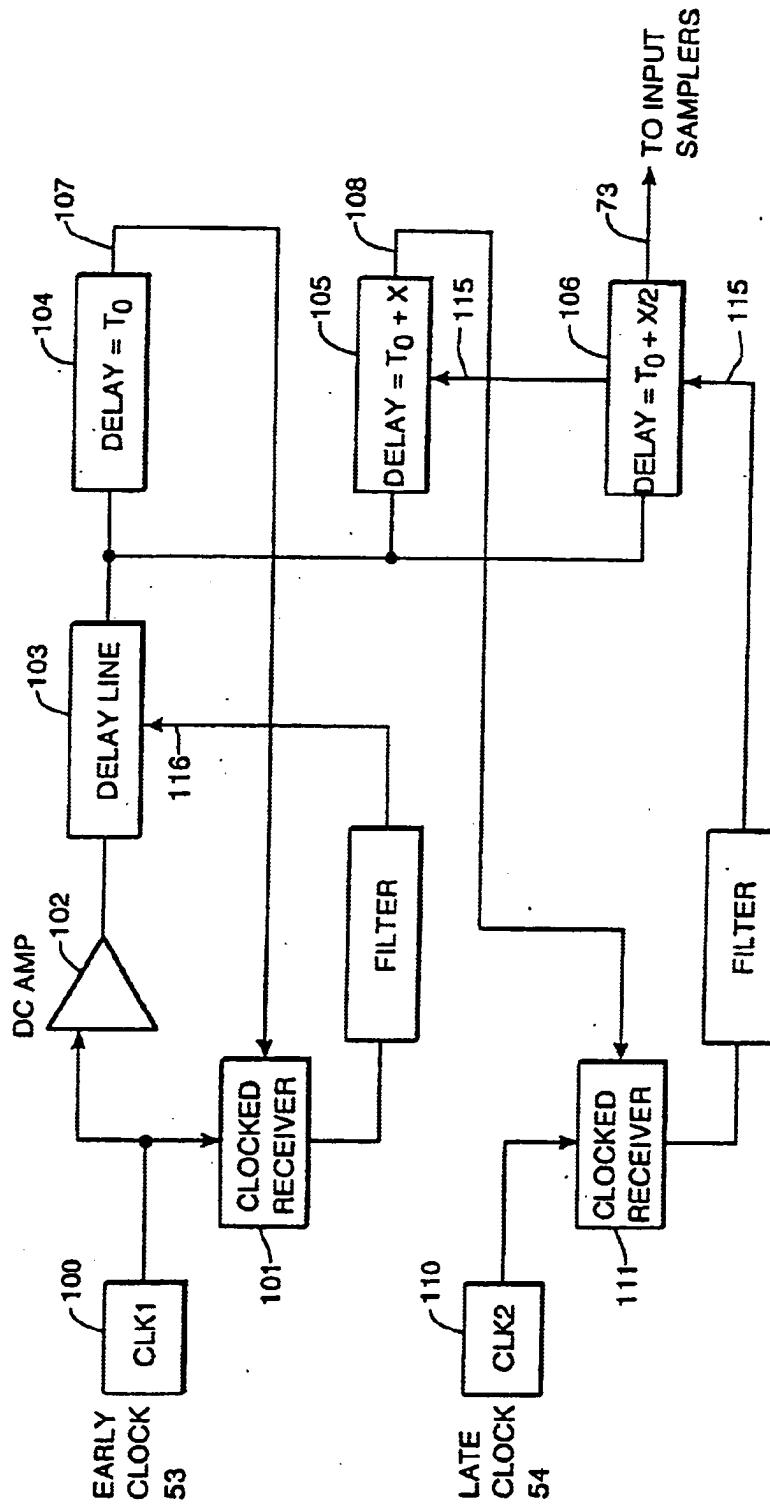
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**FIG\_ 11**



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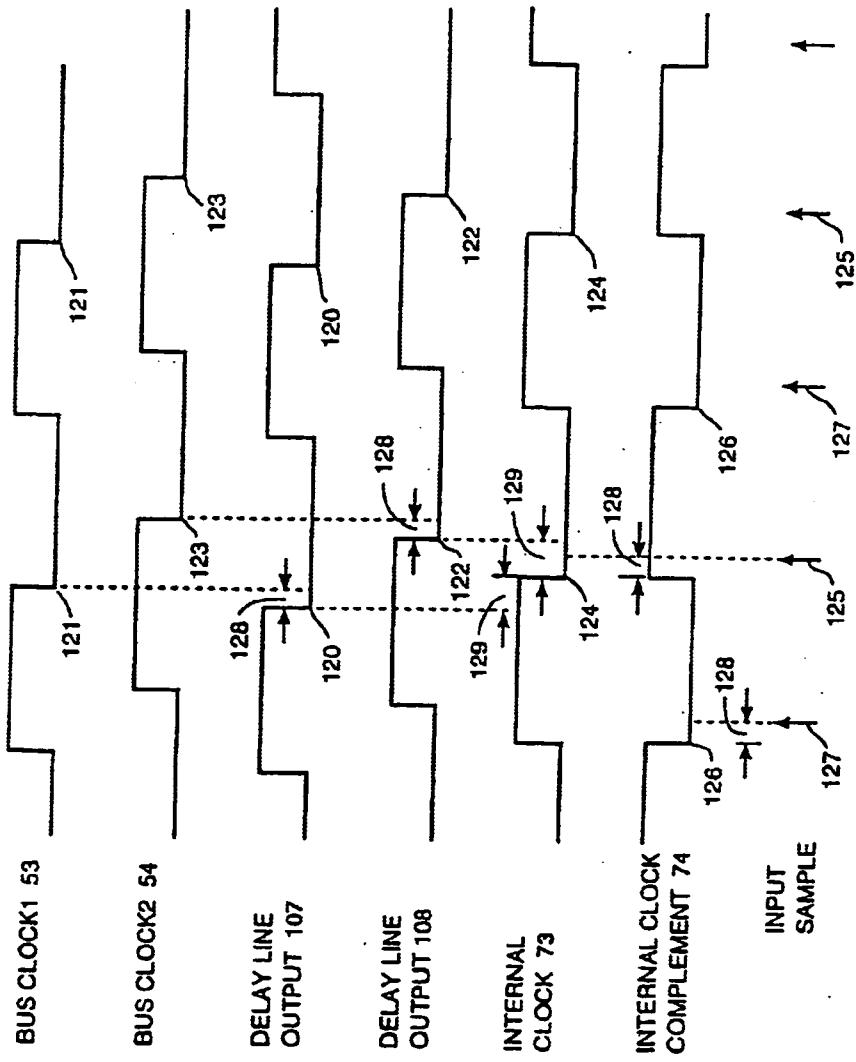
FIGURE 12



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FILE - 13

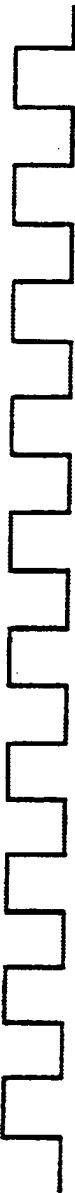


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CLOCK



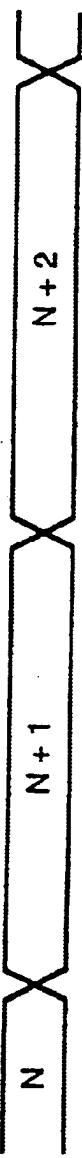
RESET IN



RESET OUT



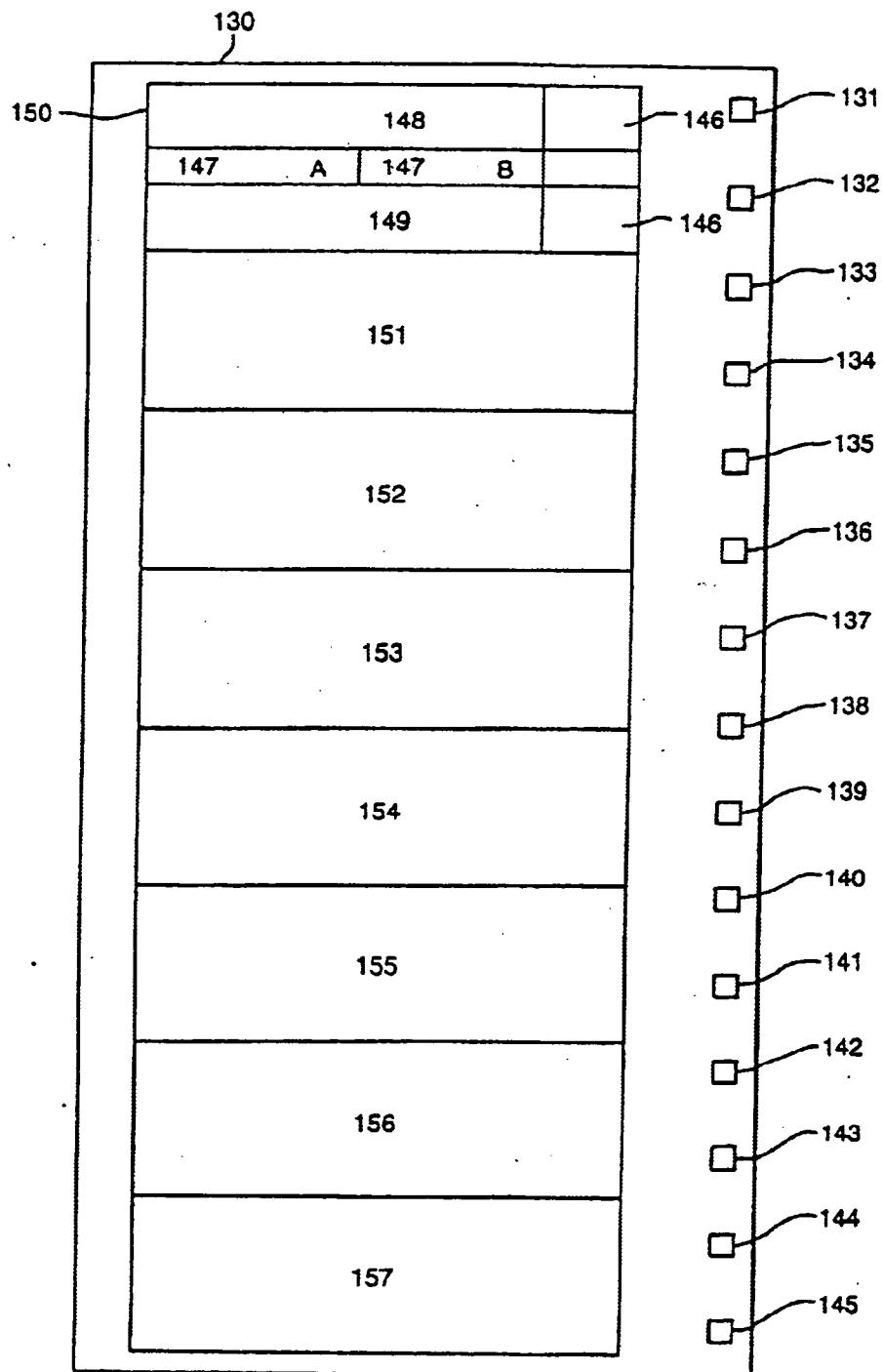
BUS DATA [0:7]      N      N + 1      N + 2



-FIGURE 1A-

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# FIG. 15



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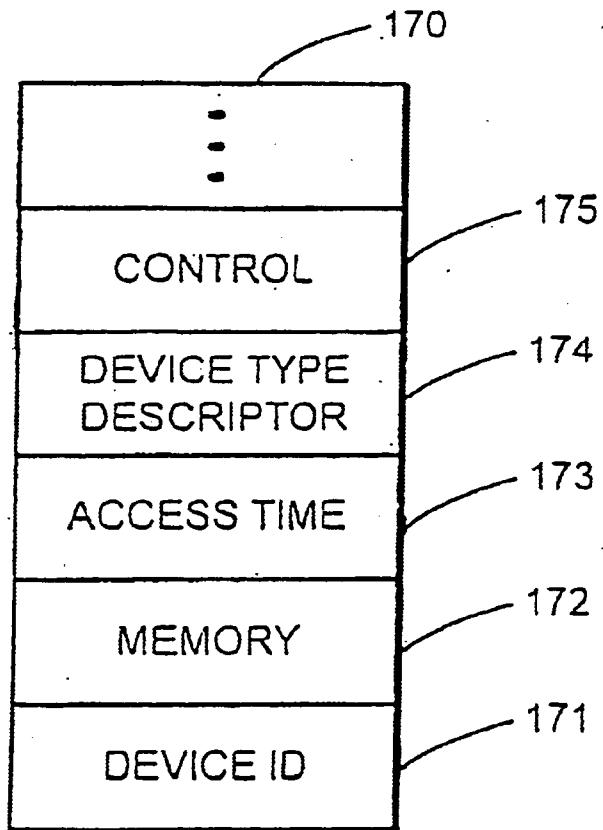


FIG. 16